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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	091654,643	
	Filing Date	September 5, 2000	
	First Named Inventor	Pak Shing Chau	
	Art Unit	Unknown	
	Examiner Name	Unknown	
Total Number of Pages in This Submission	3	Attorney Docket Number	RA-194

ENCLOSURES (Check all that apply)		
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Silicon Edge Law Group, Arthur J. Behiel
Signature	
Date	August 25, 2003

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Pak Shing Chau, Haw-Jyh Liaw, Jun Kim and Jared L. Zerbe

Assignee: Rambus, Inc.

Title: "LOW-LATENCY EQUILIZATION IN MULTI-LEVEL, MULTI-LINE COMMUNICATION SYSTEMS"

Serial No.: 09/654,643 Filed: September 5, 2000

Examiner: Unknown Tel: Unknown

Docket No.: RA-194 Art Unit: Unknown

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §1.56, §1.97 and §1.98, Applicants bring the 3 documents listed on the enclosed form PTO-1449 to the Examiner's attention in the above-captioned application. Citation of the listed documents shall not be construed as:

1. an admission that the document is necessarily prior art with respect to the instant application;
2. a representation that a search has been made; or
3. an admission that the information cited is, or is considered to be, material to patentability as defined in §1.56(b).

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Laurie Moreno

Respectfully submitted,


Arthur J. Behiel
Attorney for Applicants
Reg. No. 39,603



U.S. Department of Commerce, Patent and Trademark Office	Serial No.: 09/654,643
	Filing Date: 09/05/2000
SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Inventors: Pak Shing Chau, et al.
	Group Art Unit: Unknown
"LOW-LATENCY EQUILIZATION IN MULTI-LEVEL, MULTI-LINE COMMUNICATION SYSTEMS"	Examiner Name: Unknown
	Attorney Docket No.: RA-194

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	A						RECEIVED SEP 03 2003 Technology Center 2600
	B						
	C						
	D						
	E						

Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	F							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	G	Raghavan, S.A. et al. "Nonuniformly Spaced Tapped-Delay-Line Equalizers," IEEE Transactions on Communications, Vol. 41, No. 9, September 1993, pp 1290-1295.
	H	Ariyavisitakul, Sirikiat et al. "Reduced-Complexity Equalization Techniques for Broadband Wireless Channels," IEEE Journal on Selected Areas in Communications, Vol. 15, No. 1, January 1997, pp 5-15.
	I	Sidiropoulos, Stefanos et al. "A 700-Mb/s/pin CMOS Signaling Interface Using Current Integrating Receivers," IEEE Journal of Solid-State Circuits, Vol. 32, No. 5, May 1997, pp. 681-690.
	J	

Examiner

Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.